

OCT 14 2005

1374.43296X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: K. ICHINOSE, et al.
Serial No.: 10/733,377
Filed: December 12, 2003
For: FABRICATION METHOD OF SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE
Group: 2822
Examiner: Toniae M. Thomas
Confirm. No.: 8552

ADDITIONAL CLAIMS FEE TRANSMITTAL

Mail Stop: RCE - FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

October 14, 2005

Sir:

Transmitted herewith is an **Submission Under 37 CFR 1.114 (Amendment)** in the above-identified application.

- ☐ No additional fee is required.
☒ Also attached: **RCE Transmittal; Credit Card Payment Form**

The fee (if applicable) has been calculated as shown below:

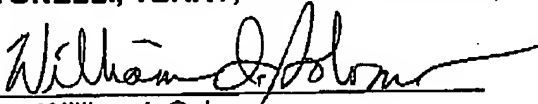
	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	30	26	4	x \$50 =	\$200.00
Independent Claims	3	3	0	x \$200 =	\$0
TOTAL FEE DUE					\$200.00

Docket No.: 1374.43296X00
Serial No. 10/733,377
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- [X] A Credit Card Payment form in the amount of \$990.00 is attached.
- [X] The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment, to Deposit Account No. 01-2135, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully Submitted,

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SUBMISSION UNDER 37 CFR 1.114
(AMENDMENT)

Mail Stop: RCE - FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

October 14, 2005

Sir:

Concurrently with the filing of a Request for Continued Examination (RCE)
Transmittal, and in response to the Office Action mailed July 14, 2005, please amend
the above-identified application as listed in the following, and as set forth on the
following pages:

AMENDMENTS TO THE CLAIMS; and

REMARKS are included following the amendments.

10/17/2005 TL0111 00000013 10733377

02 FC:1202

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